1. Open scanchain ; 4 files; but it in LAB-6
2. Set toplevel as top level entitiy(do req changes like we do in testbanch)
3. In lab 6 now, vjtag, synthesis, vjtag qip, vjtag (application waala) add to files in project
4. Compile and generate .svf files--- tools- programmer
5. Paste .svf in lab6 to urjtag, xen10 board ke folder main hai
6. Open jtag (application), cable ft2232, detect, svf filename.svf progress
7. Go to lab 6 and type cmd
8. From ppt in terminal, write scan\_vjtag.exe TRACEFILE.txt output.txt
9. Theres some ppt in scanchain waala folder in case of doubt